32-bit core

Core with 2.4GHz 32-bit Cortex M0 core, provide a powerful processing power and a familar enviornment to users.





Low power

Low power consumption of core chip, which consume 0.3µA at OFF mode and 1µA at Sleep mode.

Mesh connection

Support standard mesh standard in SDK, allowing connection between each device and extend distance.



128KB 8KB 64KB

More memory size

128KB flash, 64K SRAM and 8K cache RAM provides high flexibility and efficiency for more applications.

Time to market

Comes with serial tunneling firmware, shorten product development engineering cycle and resource.





Tiny form factor

Dimension of PRBMD02 is just 10 x 14 mm, suitable for application with limited space

Dimension (mm): 10 x 14 x 3

Antenna: chip antenna

Interfaces: PWM, PDM, I2C, SPI, UART,

JTAG, ADC

Bluetooth: 5.1 compliant protocol stack
Core: 32bit Cortex-M0 with 2.4GHz wireless

Memory: 64KB SRAM; 128kB Flash; 8KB

cache

GPIO: 19 (max), multiplexed

Firmware: serial tunneling, SDK available

Operating voltage: 1.8 - 3.6V

Current consumption: 460uA @ operate;

0.3uA @ idle

Oscillators: on board 16MHz, internal 32kHz

RC, optional external 32768Hz oscillator

Tx Power: -20 to 10dBm Sensitivity: -97dBm

Certification: FCC, CE, TELEC, KCC