

DTM mode testing guide

Introduction

This document is a translated version of DTM 模式测试操作手册 from PhyPlus. As PRBMD0x module is cored with PhyPlus chip, this document could also be a reference for PRBMD0x user. If there is any difference between this version and DTM 模式测试操作手册, please refers to DTM 模式测试操作手册.

ntroduction					
1. Firmware programming	5				
2. DTM mode testing	7				
2.1.Tx_burst_test	7				
2.2.Tx_Single_Tone	8				
2.3.Tx_Modulation	8				
2.4. Rx_Burst_Test	9				
2.5. Rx_Auto	9				

1. Firmware programming

1.1.Connect the DUT (PRBMD0x) and the computer through the UART serial port, open the PhyPlusKit software, select the serial port to be connected as shown in Figure 1, and select the baud rate (115200 for the PHY6202/6212/6222 baud rate, UXTDWU for the PHY6252 baud rate), Click connect;

🛃 PhyPlusKit File Edit Settings Help			- D X
Flash_Writer RF_CND RF_QuickSet Multi_FW Config Tim fc_Mode efuse_check Erase Size 512k Ad IMG HEX HEX HEX Ad IMG HEX HEX HEX BOOT 1(%)20210310/6222-6252-DTM 2021	/ (4000 Save dress 4.擦除flash 0310.hex 5 SEC 4.kth (件和mac1地址 110[14] 110[14] 110[14]	Clear LW Write OTP 5.烧写适件 Hex15 HexF FLA_ADDR	✓ UART Setting Port COM3 ● Baud Rate 115200 ◆ Stop Bits 1 ● Parity No ▼ D SWU Disconnect AutoCleck Update Log 1.设置串口号、波特率,点connect Name: COM3 Description:Silicon Labs CP210x USB to UART Bridge Hanifacturer: Silicon Labs Current Daudrate: 115200 Current Daudrate: 11520
Single Batch	SIZE ADDRESS	VALUE AA:AA:AA:AA:AA Mac Send ClearBuf	TimeTic Mode ASCII Save Clear

Figure 1

1.2.Set the DUT TM pin to high and press the reset key to display the green UART RX marked as 2 in Figure 1, cmd>>:Models without TM pins and reset pins (not PRBMD00 or PRBMD02), after setting the baud rate, click connect and enter the scanning state, directly reset the green UART by turning the power on and off the power line RX: cmd>>:, as shown in Figure 3;

t_Mode efuse_check E	rase Size 512k 🗸 Address		Erase	• W	rite 🗌 OTP	L) SWU	Disconnect	AutoCheck	Update
	erge					Log			
BOOT -		No OT	A .	Hex16	HexF	Name: CC	M3	IAX USP to UART Pridge	
APP ▼ 1(新)20210	310/6222-6252 -DTM 20210310.he	K 🗌 SEC	C 🗌 Auth	•	Encrypt	Manufact	urer: Silicon Labs	tex 035 to OAKT Britige	
*				FLA_ADDR		Current	port: COM3		
•				FLA_ADDR		Current	stopBits: 1		
*				FLA_ADDR		Serial o	parity: No pened!!		
•				FLA_ADDR		UART RX	: cmd>>:	***	
•				FLA_ADDR					
ChipID/IV	10[10]	т	ID[14]		CheckID				
MID[16]	SID[08]		[V[13]		writeID				
MAC[6]	KEY1[32]	K	EY2[32]		WriteMAC				
Single V Batch \									
TYPE	PATH	SIZE	ADDRESS	VALU	JE ^				
1 MAC 👻				AA:AA:AA:A	A:AA:AA				
2 •									
3									
5 -									
				Figure	2				

1.3.Enter the Flash_Write interface as shown in Figure 1, click the HEX Merge option, double-click the app bar, select the software to be burned, and enter the MAC address to be burned;

h_Writer RF_CMD	RF_QuickSet Multi_FW					UART Setti	ng		
Config	✓ Timed	ut 4000	Save	Clear	LW	Port COM3	 Baud Rate 115 	200 - Stop Bits 1	Y Parity N
_Mode efuse_check E	Erase Size 512k 🗸 Addre	ISS	Erase	Write	ОТР	🗆 swu	Disconnect	AutoCheck	Update
						Log			
BOOT -		No OTA	-	Hex16 H	lexF				
APP ▼ 1(新)20210)310/6222-6252 -DTM 202103	10.hex SEC	Auth	▼ En	crypt	Serial port	COM3 closed!!	*	
•						Current por	t: COM3		
•						Current sto	pBits: 1		
•						Serial oper	ed!!		
						UART TX ASC	II: UXTDWU	1.8	
•						UART TX ASC	II: UXTDWU		
ChipID/IV						UART TX ASC	II: UXTDWU		
PID[16]	LID[10]	TID[14]	Che	ckID	UART TX ASC	II: UXTDWU		
MID[16]	SID[08]	IV[13]	Writ	teID	UART TX ASC	II: UXTDWU		
MAC[6]	KEY1[32]	KEY2	[32]	Write	BMAC	UART TX ASC	II: UXTDWU		
						UART TX ASC UART TX ASC	II: UXTDWU II: UXTDWU		
Single Batch						UART RX: cm Current por	id>>: t: COM3		
TYPE	PATH	SIZE	ADDRESS	VALUE	^	Current bau	drate: 115200		
1 MAC 🔻				AA:AA:AA:AA:AA:AA:A	AA .	Current par	ity: No		
2 •						*******	***************		
5 -									
-									

Figure 3

1.4. Click Erase, wait until the right interface displays green Erase successfully! Click the Write button, and wait until the right interface displays green Write registers successfully! Software programming is complete, as shown in Figure 4.

h_Writer RF_CMD	RF_QuickSet Multi_FW				M UARI Se	tting		
onfig	~ Timeo	ut 4000	Save Cle	ar 🗌 LW	Port COM3	Baud Rate 115	200 V Stop Bits 1	 Parity No
_Mode efuse_check E	Erase Size 512k \checkmark Addres	ss (Erase Writ	te 🗌 OTP	swu	Disconnect	AutoCheck	Update
	lerge \				Log			
BOOT 👻		No OTA	Hex16	HexF	Receive >	>: successful!		
APP ▼ 1(新)20210	0310/6222-6252 -DTM 2021031	0.hex SEC Auth	•	Encrypt		Write hexf File [01/03]	
•			FLA ADDR		Send cpbi	n successfully! SCII: by hex mode:		
•					Receive i	mage request!		
•					Send 1mag Send chec	ksum successfully!	g to receive checksum	
-					UART RX A Receive #	SCII: checksum is: 0: OK!	x000109d3#OK>>:	
-					Receive >	<pre>>: successful!</pre>		
•			FLA_ADDK			Write hexf File [02/03]=====	
PID[16]	LID[10]	TID[14]		CheckID	Send cpbi UART RX A	n successfully! SCII: by hex mode:		
MIDIIE	510[09]	71/[12]		WeiterD	Receive i	mage request! we successful! Waiting	e to receive checksum	
MID[10]	SID[08]	17[13]		WriterD	Send chec	ksum successfully!		
MAC[6]	KEY1[32]	KEY2[32]		WriteMAC	Receive #	OK!	X00154053#UK>>:	
Single (Patch)					Receive >	<pre>>: successful!</pre>		
Single (/ Daten (Send onbi	===Write hexf File [03/03]	
TYPE	PATH	SIZE ADDRES	5 VALUE	Â	UART RX A	SCII: by hex mode:		
I MAC +			AA:AA:AA:AA	:AA:AA	Send imag	mage request: e successful! Waitin;	g to receive checksum	
2					Send chec	ksum successfully! SCII: checksum is: 0	x0031406d#0K>>:	
4 -					Receive #	OK:		
5 -					write ima	ress: 0x4000, value:	ØXAAAAAAAA	
-					Write add	<pre>Iress: 0x4004, value: isters successfully!</pre>	0x0000AAAA	
					TimeTic	Mada ACCII	- Caus	Class

Figure 4

2. DTM mode testing

2.1.Tx_burst_test

- 2.1.1.Test equipement:ROHDE&SCHWARZ FSQ
- 2.1.2.Connect DUT to computer, pull TM pin low (if available), and reset the DUT. LOG should display the following in Figure 5, which means that DTM is in fixed frequency testing mode, RF portis connected to ROHDE&SCHWARZ FSQ.

	TYPE	PATH	SIZE	ADDRESS	VALUE	^	Write images successfully! write address: 0x4000, value: 0xAAA
	MAC 🔻				AA:AA:AA:AA:AA:AA		write address: 0x4004, value: 0x000
, أ	•						Write registers successfully!
2	-					-	UARI RX :
3						_	=== SUPPORT BLE 1M ===
4	•						=== SUPPORT BLE 2M ===
5 Í	-					~	=== SUPPORT BLR 500K===



- 2.1.3.Click RF_QuickSet as shown as indication 1 in figure 6
- 2.1.4.Select Tx_BURST_TEST option in the Mode pull down menu as indication 2, it send out BLE package at fixed period and frequency. The Tx frequency can be adjusted; length (TX_Packet_Length default value is 25 Bytes); PKT default value is 0 (send out data packate format: 0->prbs9 , 1->11110000 , 2->10101010 , 3->prbs15), FREQ_OFF (frequency offset) default value is 0; Tx_Power default value is 0; TX_TPCAL is not ticked by default (core chip will auto calibrate)

File Edit Settings Help	~1				
Flash Writer RE CMD	RE QuickSet Multi FW				UART Setting
RF Setting	Mulu_1W				Port COM7 y Bau
	Mode TX BURS		→ 2		Disconnect
RF_CHN > 3	TX_BURS TX_Single TX_Modul RX_BURS RX_AUTO	T TEST 2 Tone lation T_TEST	(2426MHz)		Log Current port: COM7
Length		25	(HEX)		Current baudrate: : Current stopBits: : Current parity: No
РКТ 4		0	(HEX)		Serial opened!!
FREQ_OFF		0	(KHz)		Send erase success Receive #OK!
TX_Power		A	(HEX)	(xdBm) 🔻	Send cpnum successi Receive #OK! Receive >: success
TX_TPCAL		0	(HEX)	MANU 🗌	Send cpbin success UART RX ASCII: #0K: Beceive image peque
	Start	End			Send image success Send checksum succe UART RX ASCII: chec Receive #OK!
					Receive >>: succes: Send cobin success

Figure 5

- 2.1.5. FREQ on the instrument end is adjusted to the same as RF_CHN of the software, SPAN is adjusted to 0HZ, and RBW is adjusted to 1M, SWEEP TIME is adjusted to 8ms;
- 2.1.6. Click the Start button on the software PhyPlusKit, send the sequence command, and observe whether the instrument has packet loss;
- 2.1.7. To set up the instrument, click on the VSA-DIGITAL STANDARD-PHYPLUS-BLETIMING_1M-ENTER-TRIG-IFPOWER(-5dBm)-TRIGGEROFFSET(80us);

- 8. Change the PKT marked as 4 in the PhyplusKit software in Figure 6 to 0, 1 and 2, respectively. Observe the waveform and record the value of FSK_Meas_Dev
- 9. Drag the RF_CHN of the software label 3 in Figure 6 to 2440MHZ and 2480MHZ, and change the frequency of the instrument at the same time, and repeat the above steps 4-8;

2.2. Tx_Single_Tone

- 2.2.1.Test instrument: ROHDE&SCHWARZ FSQ
- 2.2.2.Connect the DUT to the computer with UART, pull down the TM (ignore this step for models without TM pins), and reset the DUT, the log as shown in Figure 4 will be returned, which means that the DTM fixed frequency test mode is entered, and the RF port is connected to the ROHDE&SCHWARZ FSQ of the instrument , select the Tx_Single_Tone option of the software PhyPlusKit Mode, other options are default values, transmit a single tone signal, reset the DUT, and click Start



- 2.2.3.On the instrument side, adjust FREQ to the same as RF_CHN of the software, adjust SPAN to 5M, and RBW to 50k, click MKR-PEAK-CENTER=MKR_FREQ, select the peak point of the waveform, adjust the waveform to the center of the screen, then click AMPT, rotate Turn the knob, the difference between the peak point and the REFERENCE LEVEL is about 10dB, read the transmit power value and frequency deviation at this time, and record it.
- 2.2.4.Change the software Tx_Power to MAX and EXTRA respectively, and perform step 2;
- 2.2.5.Adjust the RF_CHN on the software to 2426M, 2440M and 2480M respectively, and perform steps 2-4 above

2.3. Tx_Modulation

2.3.1.Test instrument: ROHDE&SCHWARZ FSQ

- 2.3.2. Connect the DUT to the computer via UART, and pull the TM low (for models without TM pins, ignore this step), and reset the DUT, it will return to the log as shown in Figure 4 below, which means that the DTM fixed frequency test mode is entered, and the RF port is connected to the ROHDE&SCHWARZ FSQ of the instrument;
- 2.3.3.Select Tx_Single_Tone of software PhyPlusKit Mode, other options are default values, click Start, adjust FREQ to the same as RF_CHN of the software, SPAN to 5M, RBW to 50k, click MKR-PEAK CENTER=MKR_FREQ, Select the peak point of the waveform, adjust the waveform to the middle of the screen, then click AMPT, rotate the knob, until the peak point is about 10dB away from the REFERENCE LEVEL, click the End of the software PhyPlusKit, change the test Mode to Tx_Modulation, click Start, and transmit a continuous modulation signal , click on VSA-DIGITAL_STANDARD-PHYPLUS-BLE1M, observe the eye diagram, read the FSK Meas Dev value, and record it.
- 2.3.4. Adjust the RF_CHN on the software to 2426M, 2440M and 2480M respectively, and perform the above step 2.

2.4. Rx_Burst_Test

- 2.4.1.Test instrument: ROHDE&SCHWARZ SMJ 100A
- 2.4.2. Connect the DUT to the computer with UART, pull down the TM (for models without TM pins, ignore this step), and reset the DUT, the log as shown in Figure 4 will be returned, which means that the DTM fixed frequency test mode is entered, and the RF port is connected to the instrument ROHDE&SCHWARZ SMJ 100A connect;
- 2.4.3. Open the Matlab software, execute the main_siggen.m program, and click LOCAL on the instrument side to enter the local operable state;
- 2.4.4. Select the Rx_Burst_Test option of the software PhyPlusKit Mode, other options are default values, enter the RX demodulation mode, and count the total number of received packets;
- 2.4.5. Adjust RF_CHN on software to 2426M
- 2.4.6. FREQ on the instrument side is consistent with RF_CHN, click Start of PhyPlusKit software, click End after 5s, observe the COUNT value, count the total number of received packets, FOFF is the frequency offset estimate value of RX_PHY (khz), RSSI is the received signal strength estimate value, CARR_SENS is the signal quality estimate
- 2.4.7.Change the size of the RF_CHN value on the software side to 2440 and 2480 respectively, and repeat step 6.

2.5. Rx_Auto

- 2.5.1.Test instrument: ROHDE&SCHWARZ SMJ 100A
- 2.5.2.Connect the DUT to the computer with UART, pull down the TM (for models without TM pins, ignore this step), and reset the DUT, the log as shown in Figure 4 will be returned, which means that the DTM fixed frequency test mode is entered, and the RF port is connected to the instrument ROHDE&SCHWARZ SMJ 100A connect;

- 2.5.3.Select RX_AUTO in PhyPlusKit test mode, every 1000 data packets interval, automatically count the number of received correct data packets;
- 2.5.4.RF_CHN adjusted to 2426M
- 2.5.5. Adjust the FREQ of the instrument to the same value as the RF_CHN value of the software, and the others are the default values. Click the Start button of the software PhyPlusKit to adjust the Level value on the instrument until the COUNT value is greater than and close to 700. Read the PEP value on the instrument and read it. record as the magnitude of the receive sensitivity value for this channel;

PhyPlusKit	-		_						_	
File Edit Settin	as Help				_					
Flash_Writer	RF_CMD RF_QL	ickSet Mu	lti_FW					i r 🛙	UART Sett	ing
DE Cotting								P	ort COM28	- B
Kr Setung										
РНУ	BLE 1M	Mode	RX AUTO		Ţ				Dis	connect
		,	0.0020000						og	
RF_CHN					12	(2426MHz)			response:	81 d4
									response:	80 4a
Length					25	(HEX)			response:	83 Øf
			9						response:	81 d3
									response:	80 62
PKT					0	(HEX)			response:	80 47
									response:	83 18
			-0		0	(1/11-)			response:	81 da
FREQ_OF	T I				U	(KHZ)			response:	80 62
									response:	00 47
TX Powe					Δ	(HEX)	(xdBm) 🔻		response:	81 49
						(127)	(nability		response:	80 62
									response:	80 49
TX_TPCA	ւ Մ-				0	(HEX)	MANU 📃		response:	83 12
									response:	81 d7
									response:	80 62
		Chart		5 -4					response:	80 48
		Start		End					response:	83 17
									response:	81 d8
									response:	80 62
RESPONSE									response:	00 40 83 14
TX			R	X					response:	81 d8
									response:	80 62
				COUNT			786		response:	80 4a
				COONT			700		response:	82 fd
						_			response:	81 d6
				FOFF			-41		response:	80 62
TPCAL									response:	80 4b
				RSSI			-98		response:	83 12
									response:	81 d/
						_			response:	80 62
				CARR_SENS			73		response:	00 49

2.5.6.Change the size of the RF_CHN value on the software side to 2440 and 2480 respectively, and repeat the execution of the above 5 steps.